

16 A standard SCRs

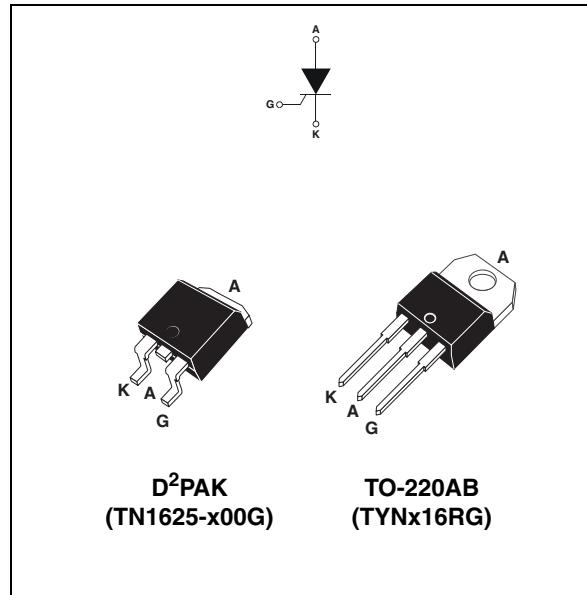
## Features

- $I_{T(RMS)} = 16 \text{ A}$
- $V_{DRM}/V_{RRM} = 600 \text{ to } 1000 \text{ V}$
- $I_{GT} = 25 \text{ mA}$

## Description

The standard TN16 / TYNx16 16 A SCRs series is suitable for general purpose applications.

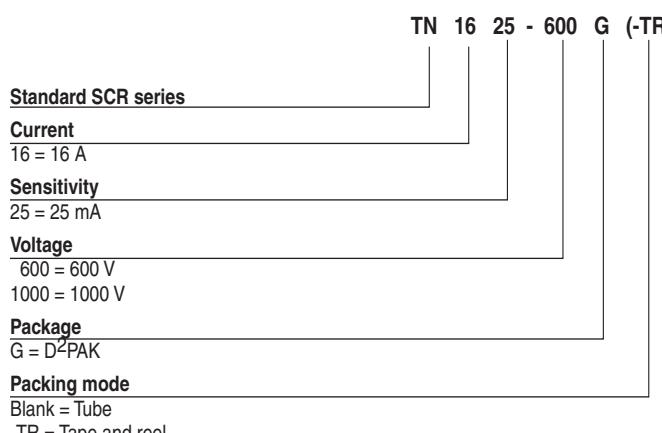
Using clip assembly technology, they provide a superior performance in surge current capabilities.



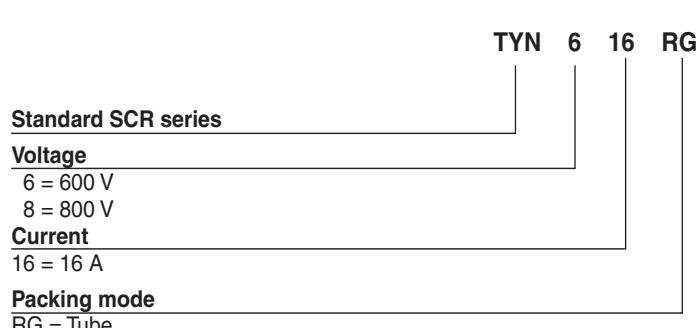
**Table 1. Device summary**

## 1 Ordering information scheme

**Figure 1. TN1625**



**Figure 2. TYNx16**



## 2 Characteristics

**Table 2. Absolute ratings (limiting values)**

Symbol	Parameter			Value	Unit
I <sub>T(RMS)</sub>	RMS on-state current (180 °Conduction angle)		T <sub>c</sub> = 110 °C	16	A
I <sub>T(AV)</sub>	Average on-state current (180 °Conduction angle)		T <sub>c</sub> = 110 °C	10	A
I <sub>TSM</sub>	Non repetitive surge peak on-state current	t <sub>p</sub> = 8.3 ms	T <sub>j</sub> = 25 °C	200	A
		t <sub>p</sub> = 10 ms		190	
I <sup>2</sup> t	I <sup>2</sup> t Value for fusing	t <sub>p</sub> = 10 ms	T <sub>j</sub> = 25 °C	180	A <sup>2</sup> s
dI/dt	Critical rate of rise of on-state current I <sub>G</sub> = 2 x I <sub>GT</sub> , t <sub>r</sub> ≤ 100 ns	F = 60 Hz	T <sub>j</sub> = 125 °C	50	A/μs
I <sub>GM</sub>	Peak gate current	t <sub>p</sub> = 20 μs	T <sub>j</sub> = 125 °C	4	A
P <sub>G(AV)</sub>	Average gate power dissipation		T <sub>j</sub> = 125 °C	1	W
T <sub>stg</sub> T <sub>j</sub>	Storage junction temperature range Operating junction temperature range			- 40 to + 150 - 40 to + 125	°C
V <sub>RGM</sub>	Maximum peak reverse gate voltage			5	V

**Table 3. Electrical characteristics (T<sub>j</sub> = 25 °C, unless otherwise specified)**

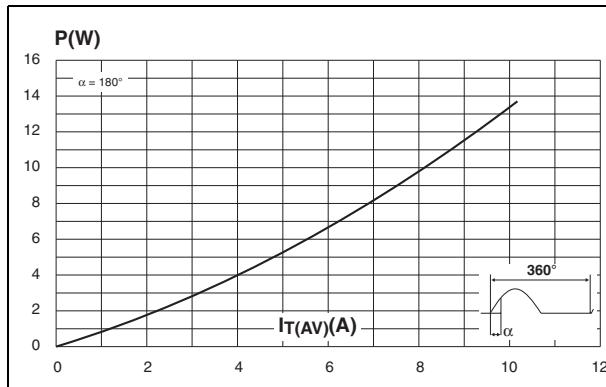
Symbol	Test Conditions		Value	Unit
I <sub>GT</sub>	V <sub>D</sub> = 12 V      R <sub>L</sub> = 33 Ω	MIN.	2	mA
		MAX.	25	
V <sub>GT</sub>		MAX.	1.3	V
V <sub>GD</sub>	V <sub>D</sub> = V <sub>DRM</sub> R <sub>L</sub> = 3.3 kΩ	T <sub>j</sub> = 125 °C	MIN.	0.2
I <sub>H</sub>	I <sub>T</sub> = 500 mA    Gate open		MAX.	40
I <sub>L</sub>	I <sub>G</sub> = 1.2 x I <sub>GT</sub>		MAX.	60
dV/dt	V <sub>D</sub> = 67 % V <sub>DRM</sub> Gate open	T <sub>j</sub> = 125 °C	MIN.	500
V <sub>TM</sub>	I <sub>TM</sub> = 32 A    t <sub>p</sub> = 380 μs	T <sub>j</sub> = 25 °C	MAX.	1.6
V <sub>t0</sub>	Threshold voltage	T <sub>j</sub> = 125 °C	MAX.	0.77
R <sub>d</sub>	Dynamic resistance	T <sub>j</sub> = 125 °C	MAX.	23
I <sub>DRM</sub> I <sub>RRM</sub>	V <sub>DRM</sub> = V <sub>RRM</sub>	T <sub>j</sub> = 25 °C	5	μA
		T <sub>j</sub> = 125 °C		

**Table 4. Thermal resistance**

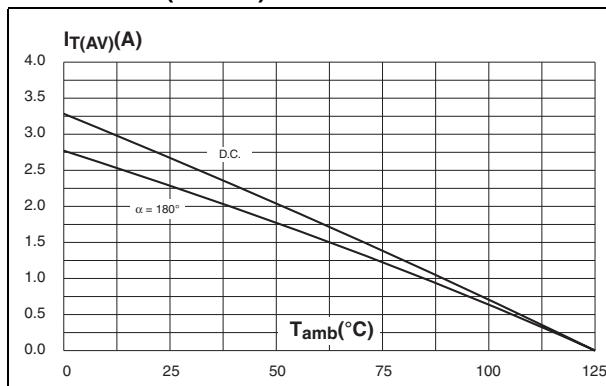
Symbol	Parameter			Value	Unit
R <sub>th(j-c)</sub>	Junction to case (DC)			1.1	°C/W
R <sub>th(j-a)</sub>	Junction to ambient (DC)	S = 01 cm <sup>2</sup>	D <sup>2</sup> PAK	45	°C/W
			TO-220AB	60	

S = copper surface under tab

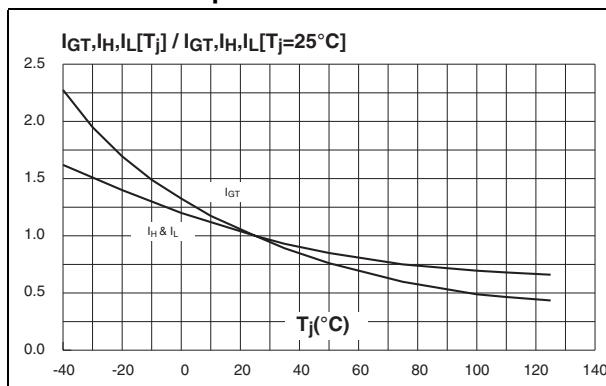
**Figure 3. Maximum average power dissipation versus average on-state current**



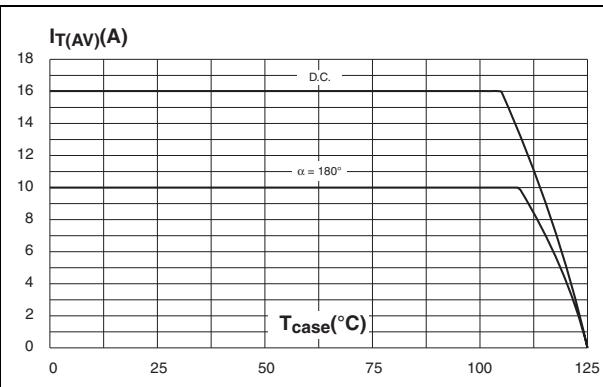
**Figure 5. Average and D.C. on-state current versus ambient temperature (copper surface under tab:  $S=1\text{cm}^2$ ) (D<sup>2</sup>PAK)**



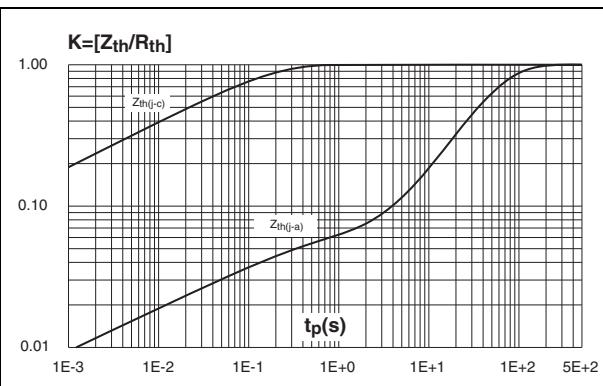
**Figure 7. Relative variation of gate trigger current, holding current and latching current versus junction temperature**



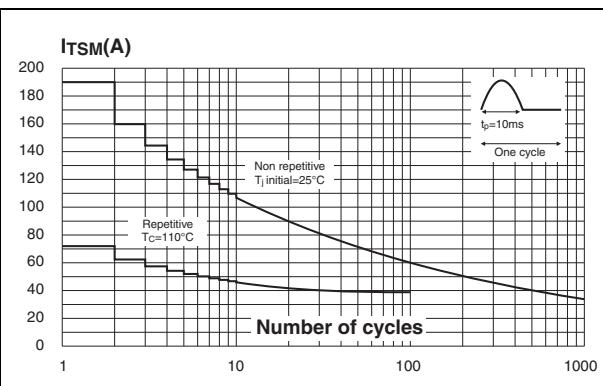
**Figure 4. Average and D.C. on-state current versus case temperature**



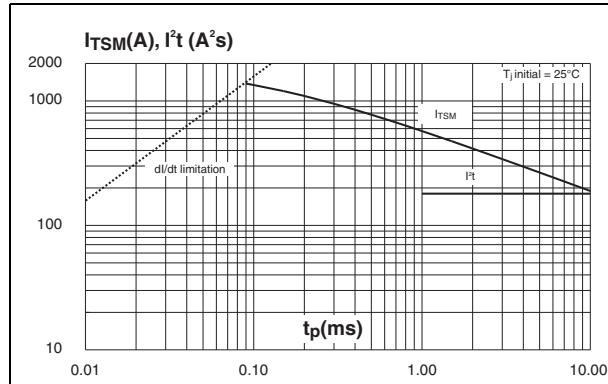
**Figure 6. Relative variation of thermal impedance versus pulse duration**



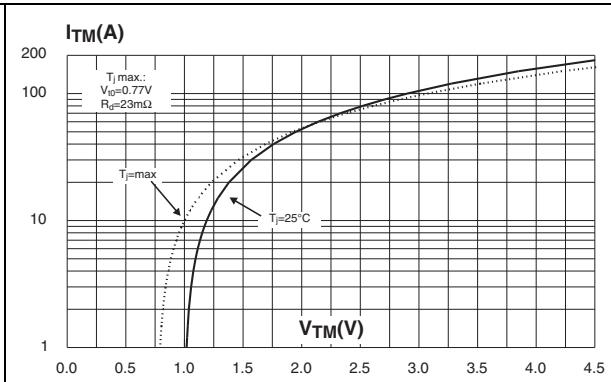
**Figure 8. Surge peak on-state current versus number of cycles**



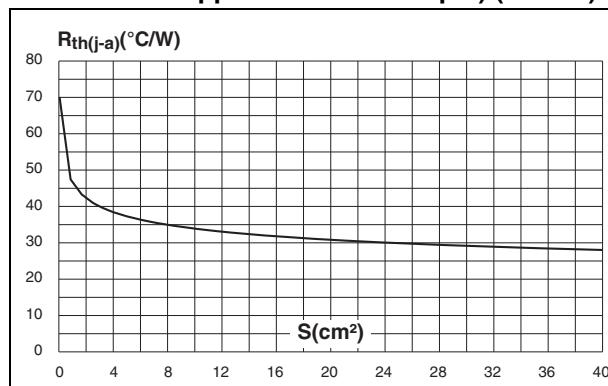
**Figure 9.** Non-repetitive surge peak on-state current for a sinusoidal pulse with width  $t_p < 10$  ms, and corresponding values of  $I^2t$



**Figure 10.** On-state characteristics (maximum values)



**Figure11.** Thermal resistance junction to ambient versus copper surface under tab  
(epoxy printed circuit board FR4,  
copper thickness: 35  $\mu\text{m}$ ) (D<sup>2</sup>PAK)

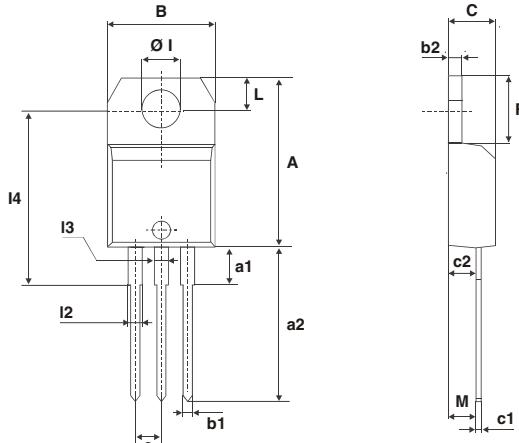


### 3 Package information

- Cooling method: C
- Recommended torque value: 0.4 - 0.6 N·m

**Table 5. TO-220AB dimensions**

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	15.20		15.90	0.598		0.625
a1		3.75			0.147	
a2	13.00		14.00	0.511		0.551
B	10.00		10.40	0.393		0.409
b1	0.61		0.88	0.024		0.034
b2	1.23		1.32	0.048		0.051
C	4.40		4.60	0.173		0.181
c1	0.49		0.70	0.019		0.027
c2	2.40		2.72	0.094		0.107
e	2.40		2.70	0.094		0.106
F	6.20		6.60	0.244		0.259
Øl	3.75		3.85	0.147		0.151
I4	15.80	16.40	16.80	0.622	0.646	0.661
L	2.65		2.95	0.104		0.116
I2	1.14		1.70	0.044		0.066
I3	1.14		1.70	0.044		0.066
M		2.60			0.102	

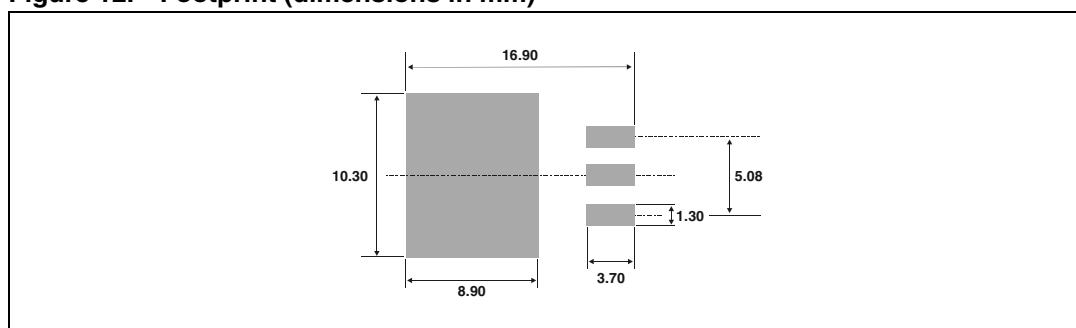


The technical drawing illustrates the physical dimensions of the TO-220AB package. The top view shows the overall height (I4), lead spacing (B), lead thickness (a1), lead pitch (a2), lead height (L), lead width (e), lead gap (I2), lead thickness (I3), and lead height (I4). The side cross-section shows the lead height (L), lead gap (I2), lead thickness (I3), lead width (e), lead thickness (I4), lead height (F), lead gap (M), lead thickness (c1), lead width (c2), lead height (c1), lead gap (b2), lead thickness (a2), lead height (b1), lead gap (a1), lead thickness (I4), lead width (B), lead height (L), lead gap (I2), lead thickness (I3), and lead width (e).

**Table 6. D<sup>2</sup>PAK dimensions**

Ref.	Dimensions					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	4.30		4.60	0.169		0.181
A1	2.49		2.69	0.098		0.106
A2	0.03		0.23	0.001		0.009
B	0.70		0.93	0.027		0.037
B2	1.25	1.40		0.048	0.055	
C	0.45		0.60	0.017		0.024
C2	1.21		1.36	0.047		0.054
D	8.95		9.35	0.352		0.368
E	10.00		10.28	0.393		0.405
G	4.88		5.28	0.192		0.208
L	15.00		15.85	0.590		0.624
L2	1.27		1.40	0.050		0.055
L3	1.40		1.75	0.055		0.069
R	0.40			0.016		
V2	0°		8°	0°		8°

**Figure 12. Footprint (dimensions in mm)**



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