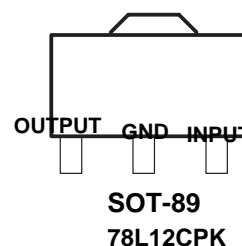
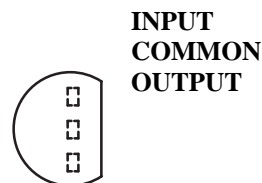
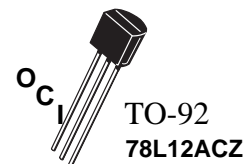


- 3-Terminal Regulators
- Output Current up to 100 mA
- No External Components
- Internal Thermal-Overload Protection 12
- Internal Short-Circuit Current Limiting
- Direct Replacements for Fairchild  $\mu$ A78L Series

**description**

This series of fixed-voltage integrated-circuit voltage regulators is designed for a wide range of applications. These applications include on-card regulation for elimination of noise and distribution problems associated with single-point regulation. In addition, they can be used with power-pass elements to make high-current voltage regulators. One of these regulators can deliver up to 100 mA of output current. The internal limiting and thermal-shutdown features of these regulators make them essentially immune to overload. When used as a replacement for a zener diode-resistor combination, an effective improvement in output impedance can be obtained, together with lower bias current.



19V,  $I_o=40mA$  (unless

**electrical characteristics at specified virtual junction temperature,  $V_{78L12}$  otherwise noted)**

PARAMETER	TEST CONDITIONS	T ‡	11.5	12	12.5	UNIT
			MIN	TYP	MAX	
Output voltage	$I_o=1mA$ to 40mA, $V_I=14V$ to 27V	25°C	11.4	12	12.6	V
	14.5V to 27V	Full range		55	250	
	$I_o=1mA$ to 270 mA	Full range		49	200	
Input voltage regulation	$V_I=15V$ to 25V,	25°C	37	42	100	
	$V_I=$					
Ripple rejection	$V_I=$ f = 120 Hz	25°C		13	50	dB
Output voltage regulation	$I_o=1$ mA to 100 mA	25°C		70		
	$I_o=1$ mA to 40 mA					
Output ripple voltage Bias current	f = 10 Hz to 100 kHz	25°C		1.7		$\mu$ V
Dropout voltage		25°C				mV
Bias current	16V to 27V	25°C			6	mA
		Full range			5.5	
		125°C				
Bias current change	$V_I=$ $I_o=1$ mA to 40 mA	range				1.5
						0.1

‡ Pulse-testing techniques maintain  $T_J$  as close to  $T_A$  as possible. Thermal effects must be taken into account separately. All characteristics are measured with a 0.33- $\mu$ F capacitor across the input and a 0.1- $\mu$ F capacitor across the output. Full range for the 78L05 is  $T_J=0^\circ C$  to  $70^\circ C$

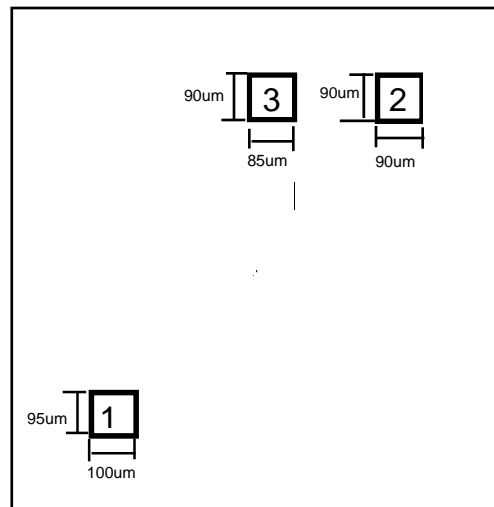
**absolute maximum ratings over operating temperature range (unless otherwise noted)**

78L12	PARAMETER	UNIT
Input voltage, $V_I$	35	V
Virtual junction temperature range, $T_J$	150	°C
Lead temperature 1,6 mm (1/16 inch) from case for 10 seconds	260	°C
Storage temperature range, $T_{stg}$	-65 to 150	°C

**recommended operating conditions**

78L12	MIN	MAX	UNIT
Input voltage, $V_I$	8	20	V
Output current, $I_O$		100	mA
Operating virtual junction temperature, $T_J$	0	70	°C

**Pad Location 78L12**



Chip size 1.0 x 1.2 mm

Pad N	Pad Name	X (um)	Y (um)
1	Ground	95	100
2	Input	820	1010
3	Output	535	1015

**IMPORTANT NOTICE – PLEASE READ CAREFULLY**

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